

FACULTY PROFILE

Faculty Name: Dr. Sappogu Vijaya Kumar

Faculty Photo:



Faculty Description:

Dr. S. Vijaya Kumar is the Professor, Department of Electrical and Electronics Engineering at Dr.K.V.Subba Reddy Institute of Technology, Kurnool. He is also the HOD of EEE at Dr.K.V.Subba Reddy Institute of Technology, Kurnool.

Profile Tab:

Qualification

| Qualification | Institution | Year |
|----------------------|--------------------------|-------------|
| Bachelor's degree | GITAM, Andhra University | 1999 |
| Master's degree | MCE, Hassan, VTU | 2002 |
| .Ph.D | JNTUA, Ananthapuramu | 2018 |

Experience

| Designation | Institution | From | To |
|--|---|-------------|-----------|
| Associate Professor & Project In-Charge | Anurag College of Engg. (Autonomous), Ananthagiri, Kodada | 2016 | 2019 |
| Associate Professor & HOD | Stanley Stephen College of Engg. & Tech, Panchalingala, Kurnool | 2007 | 2016 |
| Assistant Professor, Associate Professor & HOD | ST. Johns College of Engg & Tech, Yerrakota, Yemmiganur | 2002 | 2007 |
| Lecturer | Vasavi Polytechnic Banaganapalle. | 1999 | 2000 |

Awards / Achievement

- Ratified as Assistant Professor in 2005 at JNTUH, Hyderabad.(Stood First place)
- Attend as a resource person for Technical Symposium Dr. SGIT, Markapuram in 2017
- convener for two days national level workshop on robotics and mobile making organized by IIT Mumbai at KVSRRIT kurnool in 2019 OCT.

Responsibilities Tab:

Responsibilities

- HOD of EEE
- Time Table in charge
- Convener of Anti ragging committee
- Convener of Student Alumni committee.

Responsibilities Held

- HOD of EEE Dept from July 2003 to 2016
- As a JNTUA University Question paper setter in 2017 to 2019 .
- In charge for the Online Internal Examinations conducted by JNTU Hyderabad.
- In charge projects for B.Tech
- Handled projects for M.Tech
- In charge of Academic Plans and Time Table

Teaching Tab:

Expertise / list of subjects handled

- Under Graduate Courses: Electromechanics -I, Electromechanics -II, Electromechanics -III, Utilization of Electrical Engineering, Basic Electrical Engineering, Electrical Technology, Network Theory, Generation of electrical power, Microprocessors and Microcontrollers, Power electronics
- Post Graduate Courses: Electrical Machine Modeling, Power Converters,
- Special Topics: Designing of DC-DC converters, Special Machine, MATLAB

Research Tab:

Research Interest

- Power converters fed DC drives
- Controlling of DC drives
- FLC and ANN control in closed loop system

Research projects (current)

- Power converters Designing
- Fuzzy logic control

Publications tab:

National level Conferences

1. S.Vijaya Kumar, B.Stephen Charles “Simulation of FM-ZCS-Quasi Resonant Converter Fed DC Drive using PSPICE” Proceedings for the National Conference on Recent trend in Electrical, Electronics and Computer Engineering JCECON-06, 7th & 8th April 2006.

International level Conferences

2. S. Vijaya Kumar , S. Siva Naga Raju and B. Stephen Charles. Cascaded SEPIC – QRC Fed DC drive system with reduced current ripple, Energy procedia ELSEVIER OCTOBER – 2016.
3. Vijaya Kumar , S. Siva Naga Raju and Ch. Venkata Suresh. ANN controlled Cascaded SEPIC – QRC Fed DC drive system with Fast Dynamic Response, CITAES Dec. -2018.

International Journals

4. S. Vijaya Kumar, S. Siva Naga Raju and B. Stephen Charles “Fuzzy logic controlled cascaded SEPIC-QRC fed DC drive system with improved dynamic response”, International Journal of Advances in Engineering and Technology, (IJAET). 2016.
5. S. Vijaya Kumar, S. Siva Naga Raju and B. Stephen Charles “Improvement of dynamic time response in cascaded boost - qrc fed dc drive system using fuzzy logic controller” Paper is accepted in International Journal of Power Electronics and Drive Systems (IJPEDS). July 2017.
6. S. Vijaya Kumar, S. Siva Naga Raju and B. Stephen Charles “Intelligent Controlled Cascaded Buck-Boost – QRC Fed Dc Drive System with Wide Speed Range”, International Journal of Innovative Technologies May 2014, Vol. -02, Issue.05, ISSN 2321-8665.
7. S. Vijaya Kumar, S. Siva Naga Raju and B. Stephen Charles “Cascaded SEPIC - QRC Fed Dc Drive System with Reduced Current Ripple”, International Journal of Innovative Technologies Dec.2015, Vol. -03, Issue.11, ISSN 2321-8665.
8. S.Vijaya Kumar, S.Siva Nagaraju “LLC Resonant Converter based Bidirectional Power Flow Control” International Journal of Computational Science, Mathematics and Engineering IJCSME-Volume-I issue-6 December-2014 E-ISSN-2349-8439.

9. S.Vijaya Kumar, S.Siva Nagaraju "A Novel High-Efficiency Isolated AC-DC Converter for wind Farms using LLC Resonant Converter for Industrial Applications" International Journal of Scientific Engineering and Technology research ISSN 2319-8885 Volume-4 issue-6, March-2015, Pages 1052-1056.
10. S.Vijaya Kumar, S.Siva Nagaraju "Fuel Cell based Steady-State Model of LLC type Series-Parallel Resonant Converter with Capacitive output filter" International Journal of Scientific Engineering and Technology research ISSN 2319-8885 Volume-4 Issue-6, March-2015, Pages 1024-1029.
11. S.Vijaya Kumar, S.Siva Nagaraju "Evaluation of High Efficiency Resonant DC / DC Converter by utilizing a Resistance Compression Network" International Journal of Computational Science, Mathematics and Engineering ITCSME-Volume-2 issue-8 August-2015 E-ISSN-2349-8439.
12. S.Vijaya Kumar, S.Siva Nagaraju "Bidirectional LLC Resonant Converter with Multi Mode Transition" International Journal of Computational Science, Mathematics and Engineering ITCSME-Volume-2, issue-10 October-2015 E-ISSN-2349-8439.
13. S. Vijaya Kumar, Ch. V. Suresh "Design and tuning PID controllers for higher- order oscillatory system with improved performance" IJR Volume VIII, Issue VI, June-2019 E- ISSN NO: 2236- 6124, Page No. 4545 - 4548.
14. S. Vijaya Kumar, Ch. V. Suresh "An improved simulation of servo DC motor position control based on integral sliding mode approach" IJR Volume VII, Issue V, May-2018 E- ISSN NO: 2236- 6124, Page No. 438 - 443.
15. S. Vijaya Kumar , M. Srinu " A modified three phase battery storage system with transformer less cascaded multilevel inverter for distribution grid applications" IJR Volume VI, Issue VI, June-2017 E- ISSN NO : 2236- 6124, Page No. 170 -176.
16. P. Indusree, S. Vijaya kumar, D.Tulasi Manasa "CONSTANT CURRENT FUZZY LOGIC CONTROLLER FOR GRID CONNECTED ELECTRIC VEHICLE CHARGING", Journal of Nonlinear Analysis and Optimization: Theory & Applications Vol. 11, Issue. 1 : 2020.
17. S. Vijayakumar, A. Rajababu, V. NirmalaDevi, "POWER QUALITY IMPROVEMENT IN HYBRID POWER SYSTEM USING D-STATCOM", Material Science and Technology, 2022-23.
18. S. Vijayakumar, S. Thirumalaiah, P.Indusree, "CLOSED LOOP CONTROL OF BIDIRECTIONAL BUCK-BOOST CONVERTER IN A SMART GRID USING

PHOTOVOLTAIC AND ENERGY STORAGE SYSTEMS”, Turkish Journal of Computer and Mathematics Education (TURCOMAT), Vol.11 No.1 (2020), 1127-1138.

19. S.Vijayakumar, K.Rajesh, Y. Rajasekhar, “OPTIMIZING DG FOR DISRUPTIONS IN UNBALANCED SYSTEMS THROUGH GRID-CONNECTED CONVERSION,” POSITIF Journal, Vol 23, Issue 4, 2023-24.

FDPs and STTPs Tab:

1. Participated in a One-week National Level Online Faculty Development Programme on “Recent Trends in Green Energy Initiatives and Soft Computing Techniques” from 11-07-2023 to 17-07-2023 organized by the Department of Electrical and Electronics Engineering, Mahatma Gandhi Institute of Technology.

2. Participated in the Training program/FDP on “FIRST HAND EXPERIENCE ON SPSS – INTRODUCTORY MODULE“ Organized by faculty of commerce - Parul University, Research foundation of India & RFI care from 06 November to 12 November 2022

3. Completed all requirements of an AICTE Sponsored One Week National Level Online STTP on “Recent Trend And Challenges In Power Market With Smart Grid Technology” (PHASE-III) Organize and Electronics Engineering, 20/09/2021 TO 25/09/2021.

Patent:

Temp Num : TEMP/E1/29079/2019-CHE
Invention : INTEGRATING CASCADED CONVERTERS TO IMPROVE
THE DYNAMIC RESPONSE

Blog Tab:

Blog link: vijayakumar5888@blogspot.com

